| | Type L# | # 7 | Hits | Search Text | DBs | Time Stamp | Comme |
|---|---------|-----|-----------|---|---|---------------------|-------|
| | BRS | 7 | 3757 2 | 3757 (fpga pla pld (programmable adj logic adj USPAT; US-PGPUB; EPO; JPO; 2004/03/15 (array device))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/15 14:37 | |
| 8 | BRS | L2 | 451 | 1 and ((substrate body well backgate) near3 bias\$4) | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB 15:39 | 2004/03/15 15:39 | |
| က | BRS | L3 | 899 | 1 and (critical adj path) | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB 14:38 | 2004/03/15 14:38 | |
| 4 | BRS | L4 | 1246 6 | 1 and cost | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB 14:39 | 2004/03/15 14:39 | |
| 2 | BRS | L5 | 2 | 2 and 3 | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB | 2004/03/15 14:39 | |
| 9 | BRS | 97 | 232 | 2 and 4 | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB | 2004/03/15 15:11 | |
| | BRS | L8 | 23 | 6 and (configuration adj2 data) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/03/15 15:11 | |
| 8 | BRS | 67 | 34 | 1 and (((substrate body well backgate) near3 bias\$4) same configuration) | USPAT; US-PGPUB; EPO; JPO; 2004/03/15 DERWENT; IBM_TDB | 2004/03/15 15:40 | |